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## FREESCALE SEMICONDUCTOR LAW DEPARTMENT

DATE: February 15, 2005

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\*\*\*\*\*Docket No.: SC12793MP  
Applicant: Fuaida Harun et al.  
Serial No.: 10/613,703  
Art Unit: 2826  
Filed: July 7, 2003

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SC12793MP

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Fuaida HARUN et al.      Group Art Unit: 2826  
Application No.: 10/613,703      Examiner: Alexander O. Williams  
Date Filed: July 7, 2003  
Title: BONDING PAD FOR A PACKAGED INTEGRATED CIRCUIT

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**RESPONSE TO RESTRICTION REQUIREMENT**

MS: NON-FEE AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

SIR:

This paper is submitted in response to the Restriction Requirement mailed February 3, 2005 in the above-identified patent application. Further examination and reconsideration respectfully are requested in view of the election made below.